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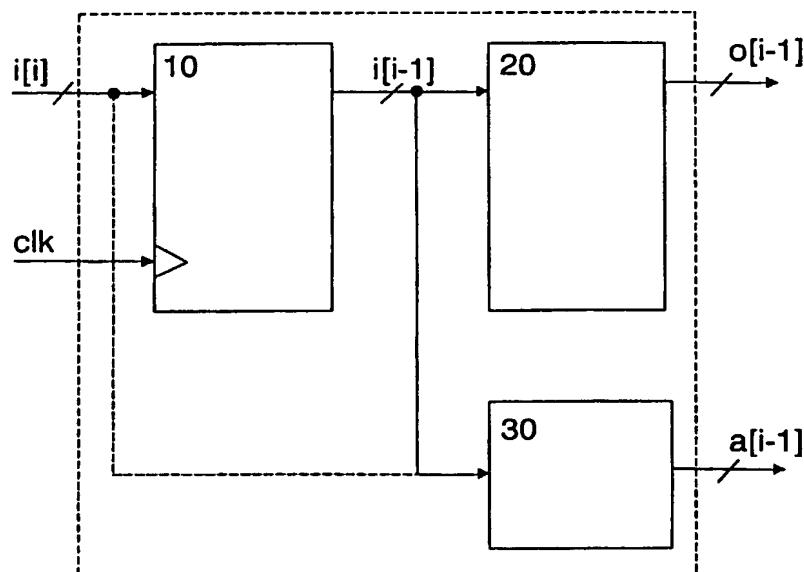
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[Continued on next page]

(54) Title: ADAPTIVE DATA PROCESSING SCHEME BASED ON DELAY FORECAST



(57) Abstract: The present invention relates to a data processing circuitry and method of processing an input data pattern and outputting an output data pattern after a processing delay which depends on a processing activity of the data processing circuitry, wherein the processing delay is estimated based on the input pattern and the processing is controlled in response to the estimated processing delay. The processing control may be a power control based on an activity monitoring or a clock control in a pipeline structure. Thereby, an efficient solution is provided to derive the current activity of the processing circuitry in order to dynamically adapt its operating conditions to its demands.

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B. FIELDS SEARCHEDMinimum documentation searched (classification system followed by classification symbols)
 IPC 7 G05B

Documentation searched other than minimum documentation to the extent that such documents are included in the fields searched

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EPO-Internal, WPI Data

C. DOCUMENTS CONSIDERED TO BE RELEVANT

Category	Citation of document, with indication, where appropriate, of the relevant passages	Relevant to claim No.
X	WO 00/35159 A (ALGOREX INC ; BAHAI AHMAD REZA (US); SANWAL KUMUD KUMAR (US)) 15 June 2000 (2000-06-15) page 2, line 18 - page 4, column 3; figures 1-3 -----	1-7, 12, 13
X	WO 01/78423 A (AIRNET COMM CORP) 18 October 2001 (2001-10-18) abstract -----	1-7, 12, 13
X	WO 99/50722 A (KRAUSE KARL HEINZ ; SIEMENS AG (DE); ZEBISCH THOMAS (DE)) 7 October 1999 (1999-10-07) page 21, line 14 - page 26, line 10; figures 1,2,6 -----	1,4-12, 14



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C.(Continuation) DOCUMENTS CONSIDERED TO BE RELEVANT

Category	Citation of document, with indication, where appropriate, of the relevant passages	Relevant to claim No.
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